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U.S. Patent Application No. 10/735,364, filed December 12, 2003, Docket No. P05762 (11461.00.5762)

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Application Number	10/735,364		
Filing Date	December 12, 2003		
First Named Inventor	Varadarajan Devnath		
Title	Transimpedance amplifier with controllable noise reduction		
Art Unit	2817		
Examiner Name	Steven J. Mottola		
Attorney Docket Number	P05762 (11461.00.5762)		

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Assignee of record of the entire interest. See 37 CFR 3.71, Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)					
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Signature				Date (8 9	105
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STATEMENT UNDER 37 CFR 3.73(b)				
Applicant/Patent Owner: Varadarajan Devnath				
Application No./Patent No.: 10/735,364 Filed/Issue Date: December 12, 2003				
Entitled: Transimpedance amplifier with controllable noise reduction				
National Semiconductor Corporation , 3 corporation , (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)				
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